



FIG. 2

52 TDMA Frames

B0	B1	B2	B3	B4	B5	B6	B7	B8	X	B9	B10	B11	X
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X = Idle frame
B0 - B11 = Radio blocks

Channel designation	Sub-channel number	Direction	Allowable time-slot assignment	Allowable RF channel assignment	Burst type	Repeat length in TDMA frames	Interleaved block TDMA frame mapping
PDTCH, PACCH		D&U	0...7	C0...Cn	NB1	52	B0(0...3), B1(4...7), B2(8...11), B3(13...16), B4(17...20), B5(21...24), B6(26...29), B7(30...33), B8(34...37), B9(39...42), B10(43...46), B11(47...50)
PBCCH		D	0...7	C0...Cn	NB	52	B0(0...3), B3(13...16), B6(26...29), B9(39...42)
PRACH		U	0...7	C0...Cn	AB	52	B0(0)...B11(11), B12(13)...B23(24), B24(26)...B35(37), B36(39)...B47(50)
PPCH, PNCH		D	0...7	C0...Cn	NB	52	B1(4...7), B2(8...11), B3(13...16), B4(17...20), B5(21...24), B6(26...29), B7(30...33), B8(34...37), B9(39...42), B10(43...46), B11(47...50)
PAGCH		D	0...7	C0...Cn	NB	52	B0(0...3), B1(4...7), B2(8...11), B3(13...16), B4(17...20), B5(21...24), B6(26...29), B7(30...33), B8(34...37), B9(39...42), B10(43...46), B11(47...50)
PTCCH/D		D	0...7	C0...Cn	NB	416	B0(12,38,64,90), B1(116,142,168,194), B2(220,246,272,298), B3(324,350,376,402)
PTCCH/U	0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15	U	0...7	C0...Cn	AB	416	B0(12) B0(38) B0(64) B0(90) B0(116) B0(142) B0(168) B0(194) B0(220) B0(246) B0(272) B0(298) B0(324) B0(350) B0(376) B0(402)

FIG. 3 Mapping of logical channels onto physical channels